

# HIGH EFFICIENCY STEP-DOWN DC/DC CONVERTER WITH ADJUSTABLE CURRENT LIMIT FOR AUTOMOTIVE APPLICATIONS

#### **DESCRIPTION**

The SD45216 is a high efficiency step down DC-DC converter with adjustable current limit in compact SOP-8 package, including an error amplifier, ramp generator, current comparator, slope compensation, current sense and logic driver. It also integrates a current error amplifier to have a constant voltage and constant current control.

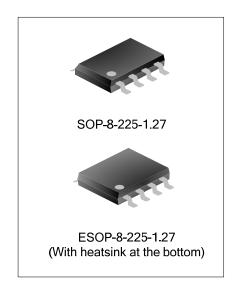
Peak current mode PWM control with external adjustable compensation provides a stable and high efficient operation over a wide range of load currents. By means of an on board current sense resistor and the availability of the current sense pins, a current limit programming is very simple and accurate.

The internal robust P-channel DMOS transistor with a typical of 100 m $\Omega$  assures high efficiency even at high output current level. The internal limiting current of typical value of 3 A, output short and over temperature protection, protect the device from accidental damage.

The internal fixed switching frequency of 120 kHz, and the SOP-8 package pin allow building an ultra compact DC/ DC converter with a minimum board space.

## **FEATURES**

- \* Up to 2A output current
- \* Up to 90% Efficiency
- \* Input Voltage Range: 8V to 40V
- \* 120 kHz Internal Jitter Frequency for lower EMI
- \* Patent -Pending Short Circuit Protection
- \* Patent -Pending Output Voltage Compensation
- \* Adjustable current limit
- \* Over Temperature Protection
- \* Small SOP-8 Package



## **APPLICATIONS**

- \* Automotive applications
- \* Chargers for NiCd, NiMH batteries
- \* Simple step-down converters with adjustable current limit
- \* Adjustable current generator

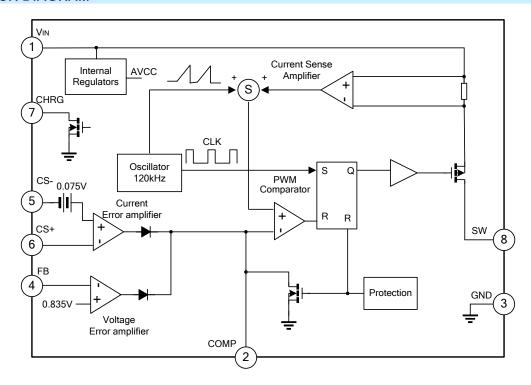
#### ORDERING INFORMATION

Part No.	Package	Marking Material		Packing	
SD45216SA	SOP-8-225-1.27	45216SA	Pb free	Tube	
SD45216SATR	SOP-8-225-1.27	45216SA Pb free		Tape&Reel	
SD45216SAG	SOP-8-225-1.27	5216SAG	5216SAG Halogen free		
SD45216SAGTR	SOP-8-225-1.27	5216SAG	Halogen free	Tape&Reel	
SD45216JA	ESOP-8-225-1.27	45216JA	Pb free	Tube	
SD45216JATR	ESOP-8-225-1.27	45216JA	Pb free	Tape&Reel	



Part No.	Package	Marking	Marking Material	
SD45216JAG	ESOP-8-225-1.27	5216JAG	Halogen free	Tube
SD45216JAGTR	ESOP-8-225-1.27	5216JAG	Halogen free	Tape&Reel

## **BLOCK DIAGRAM**



# ABSOLUTE MAXIMUM RATINGS (Tamb=25°C)

Characteristics	Rating	Unit
Vin Supply Voltage	+44	V
SW Voltage	-1~+44	V
COMP Voltage	+3	V
FB Voltage	+3	V
CS+ Voltage	-0.6~+6	V
CS- Voltage	-0.6~+6	V
Open Drain Charge Indication Pin Voltage	+10	V
Operating Temperature	-40 ~ + 85	°C
Storage Temperature	-65 ~ +125	°C

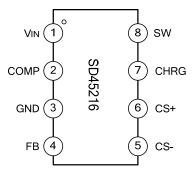
# **ELECTRICAL CHARACTERISTICS** (T<sub>amb</sub>=25°C, V<sub>IN</sub>=12V, V<sub>OUT</sub>=5V, Load Current=0 unless otherwise specified)

Characteristics	Symbol	Test condition	Min.	Тур.	Max.	Unit
Input Voltage Range	V <sub>IN</sub>	V <sub>IN</sub> pin voltage	8	I	40	>
Feedback Reference Voltage	V <sub>FB</sub>		0.815	0.835	0.855	٧
Feedback Current	I <sub>FB</sub>	V <sub>FB</sub> =0.81V		-0.1		μΑ



Characteristics	Symbol	Test condition	Min.	Тур.	Max.	Unit
Quiescent Current (Switch Off)	I <sub>switch off</sub>	V <sub>FB</sub> =1V			3	mA
Quiescent Current (PWM Active Mode)	IQ			3	4	mA
Current Sense Offset Voltage	V <sub>OFFS</sub>		70	75	80	mV
Efficiency	η	V <sub>IN</sub> =12V, V <sub>OUT</sub> =5V		90		%
Switching Frequency	Fs	V <sub>FB</sub> =0.6V	100	120	140	KHz
Max Duty Cycle	D <sub>MAX</sub>	V <sub>FB</sub> =0.6V			100	%
Pmos Switch On Resistance	Ron			0.1		Ω
Pmos Switch Leakage	I <sub>leak</sub>	V <sub>SW</sub> =0V			10	μΑ
Pmos Current Limit	I <sub>LIMIT</sub>		2.5	3	3.5	Α
UVLO Input Rising Voltage	V <sub>IN(rising)</sub>		7.8	8	8.2	V
UVLO o Voltage Hysteresis	V <sub>IN(hyst)</sub>			1.5		V
Open Drain Output Current	I <sub>OUT</sub>			1.5		mA
Thermal Shutdown Temperature	$T_{j(sd)}$			150		°C
Thermal Shutdown Hysteresis	T <sub>hyst</sub>			20		°C

# **PIN CONFIGURATION**



# **PIN DESCRIPTION**

Pin No.					
SD45216SA	SD45216JA	Pin Name	I/O	Pin Description	
1	1	VIN	Р	Input Voltage.	
2	2	COMP	I/O	Frequency compensation, resistor and capacitor connected.	
3	3	GND	G	Ground.	
4	4	FB	I	Feedback Input Pin.	
5	5	CS-	I	Comment and a Birm and a birmit and a barrier	
6	6	CS+	I	Current sense Pins, current limit resistor connected.	
7	7	CHRG	0	Charge indication Open Drain Pin.	
8	8	SW	0	Switch Pin.	
	EP	Exposed Pad	0	Exposed Pad must be connected with Switch Pin.	



#### **FUNCTION DESCRIPTION**

The SD45216 is a complete and simple step down DC-DC converter with adjustable current Limit. By means of an on board current sense resistor and the availability of the current sense pins, a current limit programming is very simple and accurate.

Moreover constant current control can be used to charge NiMH and NiCd batteries. The device can be used as a standard DC-DC converter with adjustable current limit (set by using the external sense resistor).

The internal robust P-channel MOS transistor with a typical of 100 m $\Omega$  assures high efficiency and a minimum dropout even at high output current level. The internal limiting current of typical value of 3 A protects the device from accidental overload avoiding dangerous loads damage.

When overload or output short, the main switch is turned off, thus the current offered to the load is limited, protecting the load and the device also.

When normal working, the constant current open drain transistor of CHRG is enabled, thus indication is realized by external LED, the constant current typical value is 1.5mA.

If the temperature of the chip goes higher than a fixed internal threshold (150 °C with 20 °C hysteresis), the device is turned off.

The internal fixed switching frequency of 120KHz, and the small SOP-8 package allow building an ultra compact DC-DC converter with a minimum board space.

### **APPLICATIONS INFORMATION**

#### **Output Voltage And Feedback Loop Settings**

Refer to the figure1, the output voltage of the switching regulator (V<sub>OUT</sub>) can be set with Equation following:

$$Vout = (1 + \frac{R3}{R4})X0.835V$$

The limit current is set by the external resistor R2:

$$I_{LIMIT} = \frac{75mV}{R2}$$

The SD45216 uses a patent-pending output voltage compensation scheme for the conductor wire loss by properly selecting the value of R3 R4, if the conductor resistance is Rline, current sense resistor is R2 (Refer to the figure 1), then:

$$R3 = \frac{Rline}{160 \, \mu \cdot R2}$$

$$R4 = \frac{0.835 \cdot R3}{V_{OUT} - 0.835}$$

For Rline= $60m\Omega$ , R2= $37.5m\Omega$  (2A current limit as figure 1), VOUT=5V; R3=10k, R4=2k, choose R3=10k, R4=2k as the figure 1.



#### Component Selection

#### Inductor Selection

The SD45216 can utilize small inductors due to its fast 120kHz switching frequency. Typically, a  $100\mu$ H inductor is recommended for most applications. Larger values of inductance will allow greater output current capability by reducing the inductor ripple current. Increasing the inductance above will also increase size.

The inductor current ripple is typically set for 20% to 40% of the maximum inductor current (IP). The inductor should have low ESR (series resistance of the windings) to reduce the power losses, and must be able to handle the peak inductor current without saturating. To minimize radiated noise, use a shielded bobbin inductor.

### ♦ Output and Input Capacitor Selection

Low ESR (equivalent series resistance) capacitors should be used to minimize the output voltage ripple. The parallel of multilayer ceramic and electrolytic capacitors is an excellent choice as they have extremely low ESR and are low cost. A parallel of  $10\mu\text{F}$  ceramic capacitor and  $470\mu\text{F}$  electrolytic capacitor is sufficient for most applications. Larger values may be used to obtain extremely low output voltage ripple and improve transient response.

Low ESR input capacitors reduce input switching noise and reduce the peak current drawn from the battery. It follows that ceramic capacitors are also a good choice for input decoupling, and should be located as close as possible to the device. A 10µF input capacitor is sufficient for virtually any application.

For all the ceramic capacitors above, X5R and X7R dielectric materials are preferred, for their ability to maintain capacitance over wide voltage and temperature ranges.

#### TYPICAL APPLICATION CIRCUIT

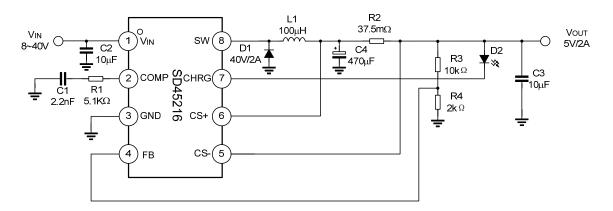
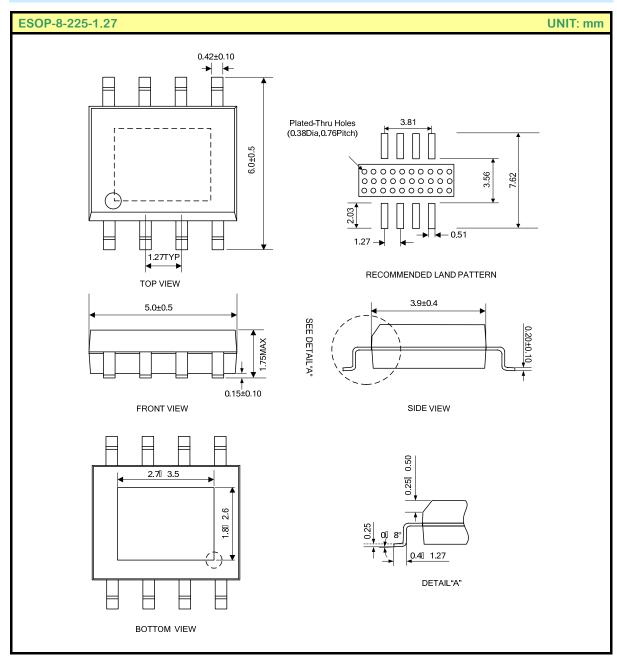


Figure 1. Application Circuit for 5V/2A Output

**Note:** The circuit and parameters are reference only, please set the parameters of the real application circuit based on the real test.



## **PACKAGE OUTLINE**

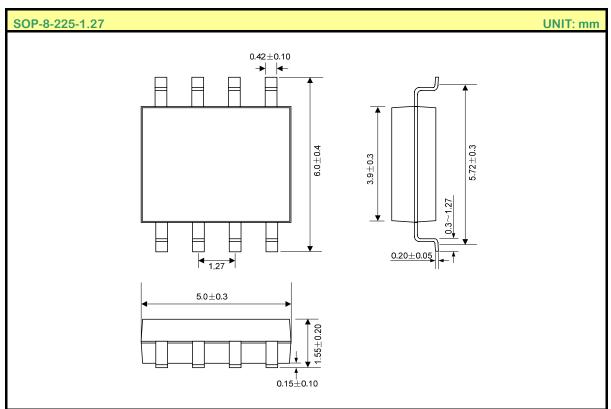


#### Note:

Please soldering together of PCB copper foil and pad which is at the bottom of ESOP package, and enlarge copper foil on PCB for heat dissipation. Better for more plated-thru holes on PCB attached to PAD.



# PACKAGE OUTLINE(continued)





## **MOS DEVICES OPERATE NOTES:**

Electrostatic charges may exist in many things. Please take following preventive measures to prevent effectively the MOS electric circuit as a result of the damage which is caused by discharge:

- The operator must put on wrist strap which should be earthed to against electrostatic.
- Equipment cases should be earthed.
- All tools used during assembly, including soldering tools and solder baths, must be earthed.
- MOS devices should be packed in antistatic/conductive containers for transportation.

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  loss of body injury or damage to property.
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# **ATTACHMENT**

# **Revision History**

Date	REV	Description	Page
2010.10.27	1.0	Original	